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PATENT

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,053,441

Issue Date: May 30, 2006

Inventor(s): Kimihiro MAEMURA

Title: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE

Docket No.: 118805

**REQUEST FOR CERTIFICATE OF CORRECTION UNDER RULE 322**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that a Certificate of Correction be issued in order to correct the errors specified in the attached copy of Certificate of Correction Form PTO-1050.

It is believed that the errors are on the part of the Patent and Trademark Office, and therefore no fee is due in relation to this matter in accordance with the provisions of 37 C.F.R. §1.322. However, should any fee be due, please charge the same against Deposit Account No. 15-0461 in order to ensure prompt issuance of a Certificate of Correction.

Respectfully submitted,

James A. Oliff  
Registration No. 27,075

Randi B. Isaacs  
Registration No. 56,046

**Certificate**  
SEP 14 2006  
**of Correction**

JAO:RBI/hms

Date: September 12, 2006

OLIFF & BERRIDGE, PLC  
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Alexandria, Virginia 22320  
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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,053,441

DATED : May 30, 2006

INVENTOR(S) : Kimihiro MAEMURA

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

(75) Inventor: **Kimihiro Maemura**, Suwa-shi (JP)

(30) Foreign Application Priority Data

Feb. 28, 2003 (JP) ..... 2003-054449

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PATENT NO. 7,053,441

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